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EE 8351 Digital Logic Circuits

Important 2mark questions

Unit I

1. Give each one example for error detecting code and error correction code.
2. Draw the DTL based NAND gate.
3. Convert $(101.01)_2$ to decimal number.

Unit II

1. Determine the exact number of half adders and full adders required for performing the addition of two binary numbers of 5-bits length each.
2. Mention the dependency of output in combinational circuits.
3. Draw the NAND gate circuit using NOT, AND & OR Gates.

Unit III

1. Write the role of master clock generator in synchronous circuits.
2. Comment about a present table counter & ripple counter.
3. Write down the characteristic table of JK flip-flop.

Unit IV

1. Define metastable state.
2. Draw the structure of PAL.
3. Draw the block diagram of asynchronous sequential circuit.

Unit V

1. State the purpose of test bench.
2. Draw the basic structure of MOS transistor.
3. List the languages that are combined together to get VHDL language.