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**CS 8351 Digital principles and system design**

**Important 2mark Questions**

**Unit I**

1. State the classification of binary codes.
2. Define Associative law.
3. Represent 3856 in BCD and 2421 code.

**Unit II**

1. Draw 1:8 Demultiplexer using two 1:4 Demultiplexers.
2. What is Propagation delay?
3. Construct a full adder using two half adders and OR gate.

**Unit III**

1. State the operation of T flip-flop.
2. Mention the different types of shift registers.
3. State the difference between latches and flipflops.

**Unit IV**

1. Draw the logic diagram and write the function table of D latch.
2. What is meant by race free condition in sequential circuits?
3. Define state table.

**Unit V**

1. List the major differences between PLA and PAL.
2. What is field programmable logic array?
3. What are error detecting codes? Give examples.