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${\bf Question\ Paper\ Code: X10368}$

B.E./B.Tech. DEGREE EXAMINATIONS, NOVEMBER/DECEMBER 2020 Fifth Semester

Electronics and Communication Engineering
EC 8553 – DISCRETE-TIME SIGNAL PROCESSING
(Common to Biomedical Engineering/Electronic Telecommunication
Engineering/Medical Electronics)
(Regulations 2017)

Time: Three Hours

Maximum: 100 Marks

Answer ALL questions

PART - A

 $(10\times2=20 \text{ Marks})$

- 1. State and prove the circular time shift property of DFT.
- 2. Justify the statement, "with zero padding, Discrete Fourier Transform can be used to perform linear filtering".
- 3. Using Bilinear transform obtain H(z) if $H(s) = \frac{1}{(s+1)^2}$ and the sampling period T = 0.1s.
- 4. Draw the frequency response of a digital Butterworth low pass filter with a cut-off frequency of 2 rad/sec.
- 5. What is the Gibb's phenomenon? Show how it can be reduced by using smooth windowing function in the design of FIR filters?
- 6. Write the Hamming Window function and outline its characteristic features.
- 7. Express 0.125 in floating point binary representation.
- 8. Outline the characteristics of error in product quantization.
- 9. >>>> List the merits of instruction pipelining.
- 10. Compare fixed point and floating point DSP processors.

PART - B

 $(5\times13=65 \text{ Marks})$

- 11. a) i) Obtain the response of a digital filter having the impulse response $h(n) = \{1, 2, 4\}$ to the input sequence $x(n) = \{1, 2\}$. (7)
 - ii) Compute the DFT of $x(n) = \cos(n\pi/4); 0 \le n \le 7$ using DIT-FFT algorithm. (6) (OR)

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X10368 -2-

- b) i) Obtain the output y(n) of a filter whose impulse response is $h(n) = \{1, 1, 1\}$ and input signal $x(n) = \{3, -1, 0, 1, 3, 2, 0, 1, 2, 1\}$ using overlap-save method. (7)
 - ii) Given $X(k) = \{36, -4 + j9.656, -4 + j4, -4 + j1.656, -4, -4 j1.656, -4 j4, -4 j9.656\}$, find x(n), using DIF-FFT algorithm. (6)
- 12. a) i) Determine the transfer function of the second order normalized analog Chebyshev low pass filter. (7)
 - ii) Determine the direct form I and II realization for a third-order IIR transfer function. (6)

$$H(z) = \frac{(0.28z^2 + 0.3z + 0.04)}{(0.5z^3 + 0.3z^2 + 0.7z - 0.2)}$$

b) Determine H(z) for a Butterworth filter satisfying the following constraints. (13)

$$\begin{split} \sqrt{0.5} & \leq \left| H(e^{j\omega}) \right| \leq 1 & 0 \leq \omega \leq \pi/2 \\ \left| H(e^{j\omega}) \right| \leq 0.2 & 0.75\pi \leq \omega \leq \pi \end{split}$$

With T = 1s. Apply impulse invariant transformation.

13. a) The desired frequency response of a low pass filter is (13)

$$H_d(e^{j\omega}) = \begin{cases} 1, -\pi/2 \le \omega \le \pi/2 \\ 0, \pi/2 \le \omega \le \pi \end{cases}$$

Determine $h_d(n)$. Also determine h(n) using the symmetric rectangular window, with window length = 7.

(OR)

b) i) Use the Fourier series method to design a low pass digital filter to approximate the ideal specifications given by

$$H(e^{j\omega}) = \begin{cases} 1, & \text{for } \left| f \right| \leq f_p \\ 0, & f_p \leq \left| f \right| \leq F/2 \end{cases}, \text{ where } f_p \text{ is the passband frequency and } F \text{ is } f_p = f_p \text{ is the passband frequency and } F \text{ is } f_p = f_p \text{ is the passband frequency and } F \text{ is } f_p = f_p \text{ is } f_p$$

the sampling frequency.

ii) Obtain FIR linear phase and cascade realizations of the system function. (6)

(7)

$$H(z) = \left(1 + \frac{1}{2}z^{-1} + z^{-2}\right) \left(1 + \frac{1}{4}z^{-1} + z^{-2}\right)$$

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-3- X10368

14. a) Discuss the effects of finite word length in the implementation of the FFT algorithms using fixed point arithmetic. (13)

(OR)

- b) Explain the effects of coefficient quantization in Direct Form Realization of IIR filter. (13)
- 15. a) i) Illustrate the addressing modes of DSP processors. (7)
 - ii) Sketch the structure of the MAC unit and DSP processor and explain its functions. (6)

(OR)

- b) i) Explain the architecture of fixed point and floating point DSP processors. (7)
 - ii) With suitable diagrams show how to implement FIR filter in DSP processor. (6)

PART – C (1×15=15 Marks)

- 16. a) The first order filter shown in Fig. 1 below is implemented in four-bit (including sign bit) fixed point two's complement fractional arithmetic. Products are rounded to four-bit representation using the input $x(n) = 0.10\delta(n)$. Determine
 - i) the first five outputs if $\alpha = 0.5$. Does the filter go into a limit cycle?
 - ii) the first five outputs if $\alpha = 0.75$. Does the filter go into a limit cycle?

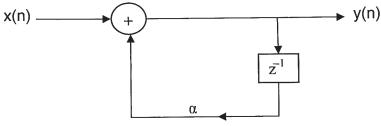


Fig. 1

(OR)

- b) i) Express the magnitude response of an FIR filter of length 11 exhibiting the linear phase property. (7)
 - ii) A band reject FIR filter of length seven is required. It is to have lower and upper cut-off frequencies as 3 KHz and 6 KHz respectively. The sampling frequency is 18 KHz. Determine the filter co-efficients using Hanning window. Draw the structure of the filter.

 (8)